

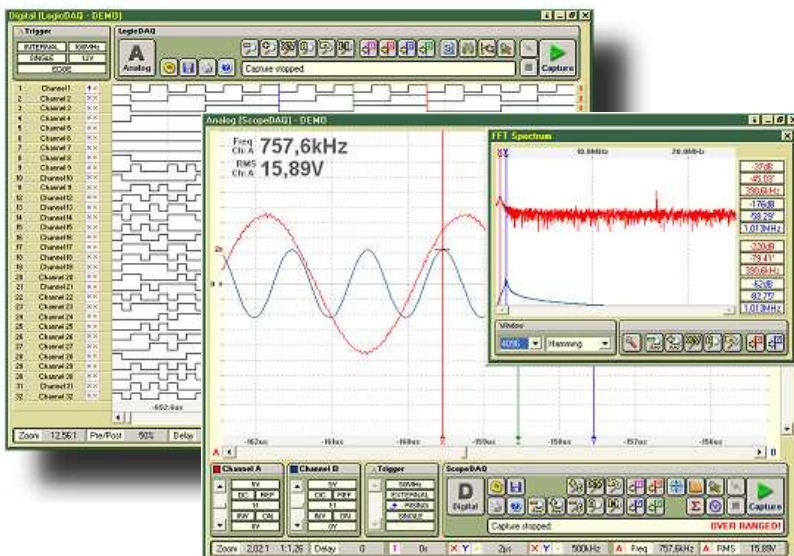
# ScopeLogicDAQ

## USB complete data acquisition system

**ScopeLogicDAQ** - USB complete data acquisition system - consists of Digital Oscilloscope and Logic Analyzer. This small instrument is making the accurate analysis of complex analog-digital signals possible - thanks to simultaneous sampling finding relationship between these signals is easy. The user is able to simultaneous use 16 digital channels and 2 analog channels with standard scope probes. Maximum frequency of sampling while using all channels is 100MHz. Higher sampling frequency are available after reducing the number of used channels. Trigger options are available both for digital and analog parts. In the analog part it is typical oscilloscope trigger with variable voltage level, in the digital part the trigger can occur on the slope or specific combination of levels of digital signals. Many features are available.



Key features:	
Sampling rates	200MHz*, 100MHz, 50MHz, 20MHz, 10MHz, 5MHz, 2MHz, 1MHz, 500kHz, 200kHz, 100kHz (* 8 digital channels, 1 analog channel)
Analog inputs	2 channels; analog gains: 5V/div, 2V/div, 1V/div, 500mV/div, 200mV/div, 100mV/div, 50mV/div, 20mV/div (5 divs, probe 1:1); maximum input voltage: 25V (probe 1:1); ADC resolution: 8-bits; analog bandwidth: 20MHz; coupling: AC, DC; input impedance: 1MOhm, 15pF
Digital inputs	16 channels maximum input voltage: from -50V to 50V
Data buffer	Variable: 256, 512, 1024, 2048, 4096, 8192, 16384, 32768, 65536, 131072, 262144*, 524288* samples (* available after memory expansion)
Trigger (analog)	Level triggering (rising or falling edge) with noise reduction feature, external trigger input.
Trigger (digital)	<i>Edge</i> - rising, falling, any; <i>Edge, skip N</i> - trigger after Nth edge (rising or falling) <i>Pattern</i> - any combination of "1", "0", "don't care" state; <i>Edge and (or) Pattern</i> - combinations of edge and pattern triggers; Trigger may be also forced by user.
Trigger level	Variable level for digital signals: 0.8V, 1.0V, 1.2V, 1.5V, 1.8V, 2.2V Variable threshold level for analog signals (full range)
Capture delay	Depending on sampling rate: for 200MHz max. 500ms, for 100MHz - 1s, etc.
Pre- and Post Trigger buffers	Variable size of Pre- and Post Trigger buffers
External clock input	Synchronized capturing with external clock. Input: digital channel 16, max. 10MHz.
Software	Easy to use Windows™ application, Most important functions and parameters directly available on main screen. Features: name/order/visibility of digital channels change, measurement cursors (voltage, time, frequency), reference signals, zoom, pattern search, signal prints, bus and state analysis feature, configuration save and many others



## Information, Availability & Pricing details

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